**Lab Sheet # 5**

Objectives:

Familiarization with 8085A microprocessor instructions related to logical and branching group.

1. WAP to load reg. pair B & D with 16 bit data. Perform the 16 bit add operation by using ADD and DAD instruction separately. Store the byte result in location C000h and C001H. Use subroutine for adding and storing.
2. WAP to check whether the no. stored in locations C000H – C009H are even or odd no. Count the no. of odd and even no. and store the even count at C00AH and odd count at C00BH.
3. WAP to transfer the data byte whose bits D3 and D6 are ON, from sequential memory location C000H –C009H to D000H onwards.
4. WAP to generate 10 Fibonacci series.
5. WAP to multiply the contents of locations C000H and C001H and store the result in location C002H and C003H.
6. WAP to divide the content of location C000H by the content of location C001H and place the result in reg. B and C.